

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application Serial No. 09/909,111
Filing Date July 18, 2001
Inventor Mark Fischer et al.
Assignee Micron Technology, Inc.
Group Art Unit 1765
Examiner D. Deo
Attorney Docket No. MI22-1777
Title: Semiconductor Processing Methods of Forming a Conductive Projection
and Methods of Increasing Alignment Tolerances

Assistant Commissioner for Patents
Washington, D.C. 20231

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Bernard Berman, Reg. No. 37,279; Wells, St. John et al.

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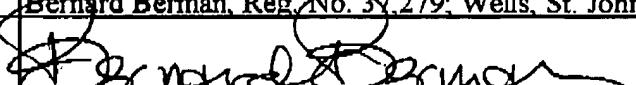
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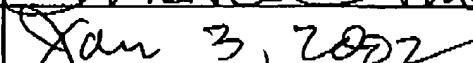
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